

University "Politehnica" of Timisoara (Universitatea "Politehnica" Timisoara)
 Faculty of Automation and Computers (Facultatea de Automatica si Calculatoare)

Domain: Computers and Information Technology (Domeniul: Calculatoare si Tehnologia Informatiei)

Title and Type of Master Programme Studies: Computer Engineering, Development of Graduation Studies (Titlul si Tipul de Master: Ingineria Calculatoarelor, Aprofundarea in domeniul Studiilor de licenta)

Type of education: Day training (Forma de invatamant: Cu frecventa)

Duration: 2 years (Durata studiilor: 2 ani)

CURRICULA - MASTER COMPUTER ENGINEERING

	SEMESTER I						SEMESTER II						SEMESTER III						SEMESTER IV																	
1.	<i>Optional 1 (choose from 9LI or 11LI)</i>						<i>Optional 1 (choose from 10LI)</i>						<i>Optional 1 (choose from 9LI or 11LI)</i>						<i>Research practical intership</i>																	
	E	9	28	0	28	0	E	9	28	0	28	0	E	9	28	0	28	0		15				98												
2.	<i>Optional 2 (choose from 9LI or 11LI)</i>						<i>Optional 2 (choose from 10LI)</i>						<i>Optional 2 (choose from 9LI or 11LI)</i>						<i>Development and Defense of Master Thesis</i>																	
	E	9	28	0	28	0	E	9	28	0	28	0	E	9	28	0	28	0		15				98												
3.	<i>Optional 3 (choose from 9LI or 11LI)</i>						<i>Optional 3 (choose from 10LI)</i>						<i>Optional 3 (choose from 9LI or 11LI)</i>																							
	E	9	28	0	28	0	E	9	28	0	28	0	E	9	28	0	28	0																		
4.	<i>Research topics in computer systems</i>						<i>Introduction to research</i>						<i>Directed thesis research</i>																							
	D	3	28	0	0	0	D	3	28	0	0	0	D	3	0	28	0	0																		
5.																																				
6.																																				
7.																																				
8.	<p align="center"><i>9 optional disciplines must be chosen (see the attached document containing optional disciplines):</i> <i>- at least 3 Breadth Coverage (BC) disciplines; - at least 3 Depth Coverage (DC) disciplines; - remaining disciplines = Advanced Electives (AE)</i></p>																																			
total / semester	hours: 196						evaluations:						hours: 196						evaluations:						hours: 196						evaluations:					
	credits: 30						3E, 1D						credits: 30						3E, 1D						credits: 30						1P					
total / week	hours: 14						hours: 14						hours: 14						hours: 14																	
	of which 8 0 6 0 (c, s, l, p)						of which 8 0 6 0 (c, s, l, p)						of which 6 2 6 0 (c, s, l, p)						of which 0 0 0 14 (c, s, l, p)																	

Legend: E = exam, D = distributed evaluation
 c = course, s = seminar, l = laboratory, p = project

RECTOR,
Prof.dr.ing. Nicolae ROBU

DECAN,
Prof.dr.ing. Octavian PROSTEAN

CURRICULA - MASTER COMPUTER ENGINEERING

	SEMESTER I						SEMESTER II						SEMESTER III						SEMESTER IV																	
1.	<i>Optional 9L1 - Testing of computer systems (BC)</i>						<i>Optional 10L1 - Advanced embedded systems (BC)</i>						<i>Optional 11L1 - Advanced digital signal processing (BC)</i>						<i>Research practical intership</i>																	
	E	9	28	0	28	0	E	9	28	0	28	0	E	9	28	0	28	0	15				98													
2.	<i>Optional 9L1 - Image processing and recognition (BC)</i>						<i>Optional 10L1 - Integrated information systems (BC)</i>						<i>Optional 11L1 - Robotic systems (BC)</i>						<i>Development and Defense of Master Thesis</i>																	
	E	9	28	0	28	0	E	9	28	0	28	0	E	9	28	0	28	0	15				98													
3.	<i>Optional 9L1 - Smart sensors and sensor networks (DC)</i>						<i>Optional 10L1 - Optic fiber transmissions (DC)</i>						<i>Optional 11L1 - Emergent and collective intelligence systems (DC)</i>																							
	E	9	28	0	28	0	E	9	28	0	28	0	E	9	28	0	28	0																		
4.	<i>Optional 9L1 - Data transmission, coding and compression (DC)</i>						<i>Optional 10L1 - Cellular data networks (DC)</i>						<i>Optional 11L1 - Evolvable hardware (DC)</i>																							
	E	9	28	0	28	0	E	9	28	0	28	0	E	9	28	0	28	0																		
5.	<i>Optional 9L1 - Emerging systems (AE)</i>						<i>Optional 10L2 - Automatic design and optimization of VLSI circuits (AE)</i>						<i>Optional 11L1 - Advanced artificial intelligence (AE)</i>																							
	E	9	28	0	28	0	E	9	28	0	28	0	E	9	28	0	28	0																		
6.	<i>Optional 9L1 - High-end interfaces and equipments (AE)</i>						<i>Optional 10L1 - Virtual measurement systems (AE)</i>						<i>Directed thesis research</i>																							
	E	9	28	0	28	0	E	9	28	0	28	0	D	3	0	28	0	0																		
7.	<i>Research topics in computer systems</i>						<i>Introduction to research</i>																													
	D	3	28	0	0	0	D	3	28	0	0	0																								
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